
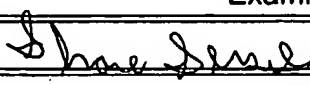


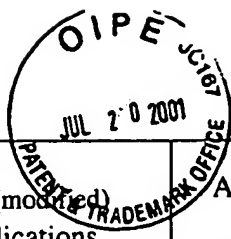


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# ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18  
Stylesheet Version v18.0

Title of Invention	Selective Zero Extension Based on Operand Size						
Application Number: 09/824869			<b>RECEIVED</b> DEC 15 2003 Technology Center 2100				
Confirmation Number: 1001							
First Named Applicant: Kevin McGrath							
Attorney Docket Number: 5500-64000							
Art Unit: 2183							
Search string: ( 4868740 ).pn.							
<p><u>Certification:</u> This Information Disclosure Statement was submitted under the following conditions, which satisfies the requirement under 37 CFR 1.97(e). The filer certified:</p> <p>That no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the information disclosure statement.</p>							
<p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p>							
init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
SG	1	4868740	1989-09-19	Kagimasa et al	—	711	2
<p><b>Signature</b></p>							
Examiner Name				Date			
				2-2-04			

**Form PTO-1449** (modified)List of Patents and Publications  
For Applicant's Information  
Disclosure Statement  
(Use several sheets if necessary)

ATTY. DOCKET NO: 5500-64000

APPLICANT: McGrath et al.

FILING DATE: April 2, 2001

SERIAL NO: 09/824,869

GROUP: 2183

**U.S. PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE

RECEIVED  
JUL 24 2001  
Technology Center 2100**FOREIGN PATENT DOCUMENTS**

EXAM. INITIALS	REF. DES.	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANSLATION. YES/NO

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

SG	A1	<u>Intel Architecture Software Developer's Manual, Volume 1: Basic Architecture</u> , Intel® Corporation, 1997, Chapter 3, pp. 5-7.					
SG	A2	<u>PowerPC™ Microprocessor Developer's Guide</u> , © 1995 by Sams Publishing, pp. 42-44, 59-61.					
SG	A3	<u>The Technology Behind Crusoe™ Processors. Low-Power x86-Compatible Processors Implemented with Code Morphing™ Software</u> , Transmeta Corporation, January 2000, pages 1-18.					
SG	A4	<u>Alpha Learns to Do Windows; Digital's FX!32 is The Key To Running Win32 Software on Alpha/Windows NT.</u> , Selinda Chiquoine, BYTE, August 4, 1998, 4 pages.					
SG	A5	<u>Awards Stack Up for DIGITAL FX!32 Windows Compatibility Software for ALPHA</u> , DIGITAL Press Releases, December 11, 1997, 7 pages.					
SG	A6	<u>DIGITAL FX!32; White Paper: How DIGITAL FX!32 Works</u> , DIGITAL Semiconductor, January 26, 1998, 4 pages.					
SG	A7	<u>An Alpha in PC Clothing; Digital Equipment's New x86 Emulator Technology Makes An Alpha System a Fast x86 Clone</u> , Tom Thompson, BYTE, August 4, 1998, 7 pages.					
SG	A8	<u>AMD 64-Bit Technology; The AMD x86 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: A, August 2000, pages 1-106.					
SG	A9	<u>AMD 64-Bit Technology; The AMD x86-64 Architecture Programmers Overview</u> , AMD, Publication #24108 Rev: C, January 2001, pages 1-128.					

EXAMINER: *Shane Dorset*

DATE CONSIDERED: 7/2/04

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the patent owner.